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2.2 Memory Area Data

The register addresses, 4000H to 453FH are used for checking and changing each set value belonging to the memory area.

Name	Register address		Number of data	Attribute	Data range	Factory set value
	HEX	DEC				
EDS transfer time (for disturbance 1)	4AC0	19136	64	R/W	0 to 3600 seconds or 0.0 to 1999.9 seconds	0
EDS transfer time (for disturbance 2)	4B00	19200	64	R/W		0
EDS action time (for disturbance 1)	4B40	19264	64	R/W	1 to 3600 seconds	600
EDS action time (for disturbance 2)	4B80	19328	64	R/W		600
EDS action wait time (for disturbance 1)	4BC0	19392	64	R/W	0.0 to 600.0 seconds	0.0
EDS action wait time (for disturbance 2)	4C00	19456	64	R/W		0.0
EDS value learning times	4C40	19520	64	R/W	0 to 10 times (0: No learning mode)	1
EDS start signal	4C80	19584	64	R/W	0: EDS start signal OFF 1: EDS start signal ON (for disturbance 1) 2: EDS start signal ON (for disturbance 2)	0
Operation mode	4CC0	19648	64	R/W	0: Unused 1: Monitor 2: Monitor + Event function 3: Control	3
Communication switch (for logic)	4D00	19712	64	R/W	b0 to b3: Communication switch 1 to 4 b4 to b15: Unused Data 0: OFF 1: ON [Decimal number: 0 to 15]	0
DO manual output	4D40	19776	64	R/W	b0 to b7: DO1 manual output to DO8 manual output b8 to b15: Unused Data 0: OFF 1: ON [Decimal number: 0 to 255]	0
DO output distribution selection	4D80	19840	64	R/W	0: DO output 1: Distribution output	0
DO output distribution bias	4DC0	19904	64	R/W	-100.0 to +100.0 %	0.0
DO output distribution ratio	4E00	19968	64	R/W	-9.999 to +9.999	1.000
DO proportioning cycle time	4E40	20032	64	R/W	0.1 to 100.0 seconds M: Relay contact output D: Open collector output	M output: 20.0 D output: 2.0
DO minimum ON/OFF time of proportioning cycle	4E80	20096	64	R/W	0 to 1000 ms	0
Unused	4EC0 ⋮ 4F80	20160 ⋮ 20352	64 ⋮ 64	—	—	—
Setting memory area number	4000	16384	64	R/W	1 to 8	1
Event 1 set value (EV1)	4040	16448	64	R/W	Deviation action, Deviation action between channels, Temperature rise completion range*: -Input span to +Input span	50
Event 2 set value (EV2)	4080	16512	64	R/W	Process action, SV action: -Input scale low to Input scale high Manipulated output value (MV): -5.0 to +105.0 %	50
Event 3 set value (EV3)	40C0	16576	64	R/W	* When temperature rise completion is selected at Event 3 action type.	50
Event 4 set value (EV4)	4100	16640	64	R/W		50
Control loop break alarm (LBA) time	4140	16704	64	R/W	0 to 7200 seconds (0: Unused)	480
LBA deadband	4180	16768	64	R/W	0 (0.0) to Input span	0 (0.0)
Set value (SV)	41C0	16832	64	R/W	Setting limiter (low) to Setting limiter (high)	TC/RTD: 0 °C [°F] V/I: 0.0 %
Proportional band [heat-side]	4200	16896	64	R/W	TC/RTD inputs: 0 (0.0, 0.00) to Input span (Unit: °C [°F]) Voltage (V)/current (I) inputs: 0.0 to 1000.0 % of Input span (0, 0.0: ON/OFF action)	TC/RTD: 30 V/I: 30.0
Integral time [heat-side]	4240	16960	64	R/W	PID control or heat/cool PID control: 0 to 3600 seconds or 0.0 to 1999.9 seconds ¹ (0, 0.0: PD action) Position proportioning control: 1 to 3600 seconds or 0.1 to 1999.9 seconds ¹	240
Derivative time [heat-side]	4280	17024	64	R/W	0 to 3600 seconds or 0.0 to 1999.9 seconds ¹ (0, 0.0: PI action)	60
Control response parameter	42C0	17088	64	R/W	0: Slow 1: Medium 2: Fast [P or PD action: 2 (Fast) fixed]	PID control, Position proportioning control: 0 Heat/cool PID control: 2
Proportional band [cool-side]	4300	17152	64	R/W	TC/RTD inputs: 1 (0.1) to Input span (Unit: °C [°F]) Voltage (V)/current (I) inputs: 0.1 to 1000.0 % of Input span	TC/RTD: 30 V/I: 30.0
Integral time [cool-side]	4340	17216	64	R/W	0 to 3600 seconds or 0.0 to 1999.9 seconds ¹ (0, 0.0: PD action)	240
Derivative time [cool-side]	4380	17280	64	R/W	0 to 3600 seconds or 0.0 to 1999.9 seconds ¹ (0, 0.0: PI action)	60
Overlap/Deadband	43C0	17344	64	R/W	TC/RTD inputs: -Input span to +Input span (Unit: °C [°F]) Voltage (V)/current (I) inputs: -100.0 to +100.0 % of Input span Minus (-) setting results in overlap. However, the overlapping range is within the proportional range.	0
Manual reset	4400	17408	64	R/W	-100.0 to +100.0 %	0.0
Setting change rate limiter (up)	4440	17472	64	R/W	0 to Input span/unit time * (0.0): Unused	0 (0.0)
Setting change rate limiter (down)	4480	17536	64	R/W	* Unit time: 60 seconds (factory set value)	0 (0.0)
Area soak time	44C0	17600	64	R/W	0 to 11999 seconds or 0 to 5999 minutes	0
Link area number	4500	17664	64	R/W	0 to 8 (0: No link)	0

¹ Varies with the setting of the integral/derivative time decimal point position selection.

2.3 COM-JL Communication Data

The register addresses, FA00H (64000) or more are used for checking and changing each set value of the COM-JL.

□ : The value of this item should always be set to 4 (Z-TIO/Z-DIO modules).

▲ : These items become valid by turning off the power of the COM-JL once, and then turning it on again after the settings are changed.

★ : This setting (factory set value: 64) causes each address to be shifted by 64 for each communication item in the SRZ (Z-TIO/Z-DIO module) Communication Data and Memory Area Data. Therefore, exercise sufficient care if you change the setting as the data mappings will also change.

Name	Register address		Number of data	Attribute	Data range	Factory set value
	HEX	DEC				
Unused	FA00 ⋮ FA07	64000 ⋮ 64007	1 ⋮ 1	—	—	—
COM-JL error code	FA08	64008	1	RO	Bit data b0: Memory backup error b1: RAM error b2: Controller configuration error b3: Unused b4: Ethernet hardware error b5 to b15: Unused Data 0: OFF 1: ON [Decimal number: 0 to 23]	—
Unused	FA09	64009	1	—	—	—
Number of connected controller in controller communication	FA0A	64010	1	RO	0 to 31	—
Number of connected channel in controller communication	FA0B	64011	1	RO	0 to 128	—
Action mode selection ▲	FA0C	64012	1	R/W	Bit data b0: Address setting 0: Continuous setting 1: Free setting b1 to b15: Unused [Decimal number: 0 to 1]	bit 0: 1 bit 1 to 15: 0 [Decimal number: 1]
Number of connectable controller channels ★	FA0D	64013	1	R/W	1 to 128	64
Transmission wait time of controller communication	FA0E	64014	1	R/W	0 to 100 ms	0
Backup memory state monitor	FA0F	64015	1	RO	0: The content of the backup memory does not coincide with that of the RAM. 1: The content of the backup memory coincides with that of the RAM	—
Unused	FA10 ⋮ FA27	64016 ⋮ 64039	1 ⋮ 1	—	—	—
No. 1 Controller type	FA28	64040	1	R/W	0 to 65534 4: Z-TIO/Z-DIO module	4
⋮	⋮	⋮	⋮	⋮	⋮	⋮
No. 31 Controller type	FA46	64070	1	R/W	0 to 65534 4: Z-TIO/Z-DIO module	4
Unused	FA47	64071	1	—	—	—
No. 1 Controller state	FA48	64072	1	RO	Bit data b0: Presence or absence of controller b1: Presence or absence of abnormal response b2 to b15: Unused Data 0: Absence 1: Presence [Decimal number: 0 to 3]	—
⋮	⋮	⋮	⋮	⋮	⋮	⋮
No. 31 Controller state	FA66	64102	1	RO	Bit data b0: Presence or absence of controller b1: Presence or absence of abnormal response b2 to b15: Unused Data 0: Absence 1: Presence [Decimal number: 0 to 3]	—
Unused	FA67	64103	1	—	—	—
No. 1 Controller address	FA68	64104	1	R/W	1 to 32 0: There is no connection controller	1
⋮	⋮	⋮	⋮	⋮	⋮	⋮
No. 31 Controller address	FA86	64134	1	R/W	1 to 32 0: There is no connection controller	31
Automatic acquisition of controller address	FA87	64135	1	R/W	0: Do not execute the automatic acquisition 1: Execute the automatic acquisition * * Automatically reverts to 0 after automatic acquisition ends.	0

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